

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

- 1 1. (Currently Amended) A circuit for operating a transistor as a rectifier, said circuit
2 comprising:
3 a transistor;
4 a control circuit operating said transistor as a function of the Vds voltage potential of
5 said transistor and [[a Vds]] an indication signal during a particular cycle.
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- 1 2. (Currently Amended) A circuit as recited in claim 1 wherein said [[Vds]] indication
2 signal is the on-time or off-time duration of said Vds voltage potential in a previous cycle
3 and the on-time and off-time duration of a subsequent cycle is adjusted as a function of said
4 on-time duration of said Vds voltage potential in said previous cycle.
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- 1 3. (Cancelled).
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- 1 4. (Currently Amended) A circuit as recited in claim 1 wherein said [[Vds]] indication
2 signal is the on-time or off-time duration of said Vds voltage potential in a present cycle and
3 the on-time and off-time duration of a subsequent cycle is adjusted as a function of said on-
4 time duration of said Vds voltage potential in said present cycle.
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- 1 5 - 20. Claims 5-20 (Cancelled).

1 21. (New) A circuit as recited in claim 2 wherein said indication signal is the on-time or
2 off-time duration of said Vds voltage potential in a present cycle and the on-time and off-
3 time duration of a subsequent cycle is adjusted as a function of said on-time duration of said
4 Vds voltage potential in said present cycle.

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1 22. (New) A circuit for operating a transistor as a rectifier, said circuit comprising:
2 a transistor;
3 a control circuit operating said transistor as a function of the Vds current of said
4 transistor and an indication signal during a particular cycle.

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1 23. (New) A circuit as recited in claim 22 wherein said indication signal is the on-time
2 or off-time duration of said Vds current in a previous cycle and the on-time and off-time
3 duration of a subsequent cycle is adjusted as a function of said on-time duration of said Vds
4 current in said previous cycle.

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1 24. (New) A circuit as recited in claim 22 wherein said indication signal is the on-time
2 or off-time duration of said Vds current in a present cycle and the on-time and off-time
3 duration of a subsequent cycle is adjusted as a function of said on-time duration of said Vds
4 current in said present cycle.

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1 25. (New) A circuit as recited in claim 23 wherein said indication signal is the on-time
2 or off-time duration of said Vds current in a present cycle and the on-time and off-time

3 duration of a subsequent cycle is adjusted as a function of said on-time duration of said Vds
4 current in said present cycle.

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1 26. (New) A method for operating a transistor as a rectifier, comprising the steps of:
2 detecting the Vds signal of a transistor; and
3 operating said transistor as a function of the Vds voltage potential of said transistor
4 and an indication signal during a particular cycle.

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1 27. (New) A method as recited in claim 26 wherein said indication signal is the on-time
2 or off-time duration of the Vds voltage potential in a previous cycle and the on-time and off-
3 time duration of a subsequent cycle is adjusted as a function of said on-time duration of said
4 Vds voltage potential in said previous cycle.

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1 28. (New) A method as recited in claim 26 wherein said indication signal is the on-time
2 or off-time duration of the Vds voltage potential in a present cycle and the on-time and off-
3 time duration of a subsequent cycle is adjusted as a function of said on-time duration of said
4 Vds voltage potential in said present cycle.

1 29. (New) A circuit as recited in claim 27 wherein said indication signal is the on-time
2 or off-time duration of the Vds voltage potential in a present cycle and the on-time and off-
3 time duration of a subsequent cycle is adjusted as a function of said on-time duration of said
4 Vds voltage potential in said present cycle.

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1 30. (New) A method for operating a transistor as a rectifier, comprising the steps of:
2 detecting the Vds signal of a transistor; and
3 operating said transistor as a function of the Vds current of said transistor and an
4 indication signal during a particular cycle.

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1 31. (New) A circuit as recited in claim 30 wherein said indication signal is the on-time
2 or off-time duration of said Vds current in a previous cycle and the on-time and off-time
3 duration of a subsequent cycle is adjusted as a function of said on-time duration of said Vds
4 current in said previous cycle.

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1 32. (New) A circuit as recited in claim 30 wherein said indication signal is the on-time
2 or off-time duration of said Vds current in a present cycle and the on-time and off-time
3 duration of a subsequent cycle is adjusted as a function of said on-time duration of said Vds
4 current in said present cycle.

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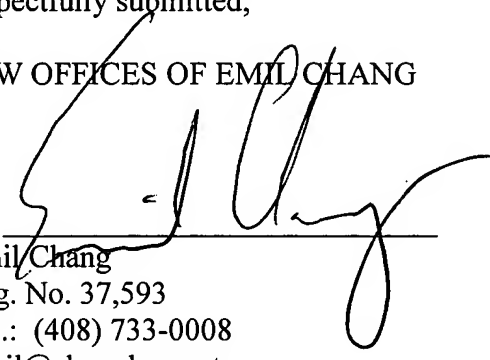
1 33. (New) A circuit as recited in claim 31 wherein said indication signal is the on-time
2 or off-time duration of said Vds current in a present cycle and the on-time and off-time
3 duration of a subsequent cycle is adjusted as a function of said on-time duration of said Vds
4 current in said present cycle.

Conclusion

Applicants have amended the claims to properly present the claims for examination. Applicants respectfully request that the Examiner considers the claims and allows the application. If any matters can be resolved by telephone, applicants request that the Patent and Trademark Office calls the applicant at the telephone number listed below. Thank you.

Respectfully submitted,

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